Developed by
GUSTECH

Analog Switches Tiny Pack Survey for SER

Part No	*Source*	*$Vsin_{min}$ in Volts*	*$Vsin_{max}$ in Volts*	*$RDS_{on\ typical}\Omega$*	*RDS_{on} Application**	*Off Isolation (dB# @ Rload Ω's @ Freq in MHz)*	*On Flatness Ω*	*Configuration*	*Package*	*DigiKey#*	*Quantity***	*Price ea.*	*Notes*
ADG779	Analog Devices	1.800	5.500	6.0	3.0	-87, 50, 1	0.750	SPDT	SC70-6	ADG779BKSZ-REEL7CT-ND	912	$1.60	
ADG849	Analog Devices	1.800	5.500	0.7	0.5	-64, 50, 0.1	0.220	SPDT	SC70-6	ADG849YKSZ500RLCT-ND	468	$1.95	
~~ISL53L110~~	~~Intersil~~	~~1.100~~	~~4.500~~	~~0.2~~		~~-65, 50, 0.1~~	~~0.030~~	~~SPST-NO~~	~~SC70-5~~	~~ISL43L110IHZ-TCT-ND~~	~~2,898~~	~~$1.59~~	~~Supply Voltage Maximum is too low~~
~~ISL84715~~	~~Intersil~~	~~1.650~~	~~3.600~~	~~0.3~~		~~-45, 50, 1~~	~~0.038~~	~~SPST-NO~~	~~SC70-5~~	~~ISL84715IH-CT-ND~~	~~1,578~~	~~$1.61~~	~~Supply Voltage Maximum is too low~~
FSA1156	Fairchild	1.650	5.500	0.8	0.8	-65, 50, 1	0.300	SPST-NO	SC70-6	FSA1156P6XCT-ND	376	$0.76	μPAK would be smaller stil
~~FSA2156~~	~~Fairchild~~	~~1.650~~	~~4.300~~	~~0.7~~		~~-65, 50, 0.1~~	~~0.250~~	~~SPST-NO~~	~~SC70-6~~	~~FSA2156P6XCT-ND~~	~~2,480~~	~~$0.90~~	~~Supply Voltage Maximum is too low~~
MAX4599	Maxim	2.000	5.500	40.0	50.0	-76, 50, 1	4.000	SPDT	SC70-6	MAX4599EXT+TCT-ND	837	$1.56	
PI5A3157	Pericom	1.650	5.500	6.0	13.0	-57,50, 250	6.000	SPDT	TDFN-6	PI5A315ZAEXCT-ND	3,240	$0.41	smallest listed; very high speed
MAX4594	Texas Instruments	2.000	5.500	10.0	4.5	-80, 50, 1	1.500	SPST-NO	SC70-5	296017650-1-ND	2,965	$0.78	other pinouts: 4596,
MAX4596	Texas Instruments	2.000	5.500	10.0	4.5	-83, 50, 1	1.500	SPST-NO	SC70-5	296-1754-1-ND	3,512	$0.78	
TS5A1066	Texas Instruments	1.650	5.500	7.5	8.5	-68, 50, 10	2.500	SPST-NO	SC70-5	296-17586-1-ND	384	$0.40	
TS5A3166	Texas Instruments	1.650	5.500	0.9	0.8	-64, 50, 1	0.150	SPST-NO	SC70-5	296-17741-1-ND	4,198	$0.48	
As the switch for the Shunt Resistor Calibrator, RDSon is a non-issue (repeatability is more important); the tiniest package is the most significant parameter for this function.													

RDSon Application* Vdd=5.2v (max. by design); Vs=+5.2 (maximum Source Voltage possible)$_A$ +85°C (worst case); derived from data sheets' curves. Quantity** as of Aug 07